

AAAAAA CCCCCCCC TTTTTTTT SSSSSSSS TTTTTTTT AAAAAA
AAAAAA CCCCCCCC TTTTTTTT SSSSSSSS TTTTTTTT AAAAAA
AA AA CC TT SS TT AA AA
AA AA CC TT SSSSSS TT AA AA
AA AA CC TT SSSSSS TT AA AA
AAAAAA CC TT SS TT AAAAAA
AAAAAA CC TT SS TT AAAAAA
AA AA CC TT SS TT AA AA
AA AA CC TT SS TT AA AA
AA AA CCCCCCCC TT SSSSSSSS TT AA AA
AA AA CCCCCCCC TT SSSSSSSS TT AA AA

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0000 1 .TITLE MAC\$ACTSTA MACHINE STATEMENTS
0000 2 .IDENT 'V04-000'
0000 3 .
0000 4 .
0000 5 *****
0000 6 *
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0000 24 *
0000 25 *
0000 26 *****
0000 27 .
0000 28 .
0000 29 ++
0000 30 : FACILITY: VAX MACRO ASSEMBLER OBJECT LIBRARY
0000 31 :
0000 32 : ABSTRACT:
0000 33 :
0000 34 : The VAX-11 MACRO assembler translates MACRO-32 source code into object
0000 35 : modules for input to the VAX-11 LINKER.
0000 36 :
0000 37 : ENVIRONMENT: USER MODE
0000 38 :
0000 39 : AUTHOR: Benn Schreiber, CREATION DATE: 25-AUG-78
0000 40 :
0000 41 : MODIFIED BY:
0000 42 :
0000 43 : V03-002 MTR0034 Mike Rhodes 03-Jun-1983
0000 44 : Set SYMSM_REF in the current PSECT block when
0000 45 : a .MASK directive is encountered.
0000 46 :
0000 47 : V03.01 MTR0017 Mike Rhodes 07-Jun-1982
0000 48 : Re-enable FLGSV_COMPEXPR in DATARG::, which
0000 49 : was disabled when a forward reference to a
0000 50 : symbol in an expression occurred.
0000 51 :
0000 52 : V02.18 BLS0063 Benn Schreiber 30-Jul-1981
0000 53 : Remove 65K store repeated check since linker
0000 54 : allows more
0000 55 :
0000 56 : V02.17 PCG0004 Peter George 28-Jul-1981
0000 57 : Call DATARG from QUDSTR and OCTSTR.

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V02.16 PCG0002 Peter George 05-May-1981
Set RELPSECT flag for all global symbol assignments
and for all global labels.

V02.15 CNH0042 Chris Hume 28-Oct-1980
De-optimize boundary valued backward references if indexing
requested. Allow the architecturally legal immediate mode in
address and yield contexts and also the practically useless
indexed immediate mode.
(ACTREF.MAR 02.15, DEFINE.MAR 02.17, SYMTAB.MAR 02.18)

V01.14 RN0023 R. Newland 3-Nov-1979
New message codes to get error messages from system
message file.

V01.13 RN0020 R. Newland 26-Oct-1979
Change error message for .BLKx expression not absolute

V01.12 RN0019 R. Newland 25-Oct-1979
Improve error pointer positioning

V01.11 RN0014 R. Newland 14-Oct-1979
Support for G_floating, H_floating and Octaword data types.
.BLKG, .BLKH, .BLKO and .OCTA directives.

V01.10 RN0005 R. Newland 12-Aug-1979
Remove .ALIGN LONG statements

V01.15 RN0029 R. Newland 12-Feb-1980
Correct listing of branch operand when on continued line.

V01.13 RN0021 R. Newland 28-Oct-1979
Correct listing of .ENTRY register mask value.
SPR 11-26384

V01.09 0003 B. Schreiber 10-JAN-1979
Catch syntax error if pound sign forgotten before
ASCII immediate (^A) in operands.

```

0000 99 .SBTTL DECLARATIONS
0000 100 ; INCLUDE FILES:
0000 101 ;
0000 102 ;
0000 103 ;
0000 104 ;
0000 105 ; MACROS:
0000 106 ;
0000 107 ;
0000 108 $MAC$SYMBLKDEF ;DEFINE SYMBOL BLOCK OFFSETS
0000 109 $MAC$CTLFLGDEF ;DEFINE CONTROL FLAGS
0000 110 $MAC$GENVALDEF ;DEFINE GENERAL VALUES
0000 111 $MAC$INTCODDEF ;DEFINE INT. FILE COMMANDS
0000 112 $MAC$ADRMODEDEF ;DEFINE ADDRESSING MODES
0000 113 $MAC$OPRDEF ;DEFINE OPERAND DESCRIPTOR BITS
0000 114 $MAC$MSGDEF ; Define message codes
0000 115 ;
0000 116 ;
0000 117 ; EQUATED SYMBOLS:
0000 118 ;
0000 119 ;
0000 120 ;
0000 121 ; OWN STORAGE:
0000 122 ;
0000 123 ;
00000000 124 .PSECT MAC$RO_DATA,NOWRT,NOEXE,GBL,LONG
0000 125 ;
0000 126 DAT_NUL_CMD:
26 0000 127 .BYTE INT$_STIB,-
0001 128 INT$_STIW,-
27 0001 129 INT$_STIL,-
28 0002 130 INT$_STIL,-
28 0003 131 INT$_STIB,-
26 0004 132 INT$_STIW,-
28 27 0005 133 INT$_STIL
0007 134 ;
0007 135 DAT_RPT_CMD:
0007 136 .BYTE INT$_STRB,-
2F 0007 137 INT$_STRW,-
30 0008 138 INT$_STRL,-
31 0009 139 0,-
00 000A 140 INT$_STRSB,-
32 000B 141 INT$_STRSW,-
00 33 000C 142 0
000E 143 ;
000E 144 DAT_STO_CMD:
000E 145 .BYTE INT$_STOB,-
34 000E 146 INT$_STOW,-
35 000F 147 INT$_STOL,-
2E 0010 148 INT$_STOL,-
2E 0011 149 INT$_STS,-
36 0012 150 INT$_STS,-
2E 37 0013 151 INT$_STOL
0015 152 ;
0015 153 DAT_TRUNC_CHK: ;ROUTINES TO CHECK FOR TRUNCATION
00000000* 0015 154 .ADDRESS MAC$CK_BYT_TRU1,- ;BYTE
0015 155 MAC$CK_WRD_TRU1,- ;WORD

```

00000000' 0019	156	0,-	:LONGWORD
00000000' 001D	157	0,-	:QUADWORD
00000000' 0021	158	MAC\$CK_SBY_TRU1,-	:SIGNED BYTE
00000000' 0025	159	MAC\$CK_SWD_TRU1,-	:SIGNED WORD
00000000' 00000000' 0029	160	0	:OCTAWORD
	0031	161	
	0031	162 DAT_SHIFT_FACT:	:SHIFT # OF ELEMENTS OF ALLOCATION
	0031	163	:BY THIS MUCH TO GET ALLOCATION
04 01 00 03 02 01 00	0031	164	:BYTE,WORD,LONG,QUAD,SIGNED_BYTE,
	0038	165	:SIGNED_WORD,OCTAWORD

0038 167 :++
0038 168 : THIS IS THE HEART OF THE MARS ASSEMBLER. THESE ROUTINES HANDLE
0038 169 : MACHINE INSTRUCTIONS WHICH APPEAR AS SPECIAL BLOCKS IN THE
0038 170 : SYMBOL TABLE. THE 'SYMSB SEG' BYTE IS THE NUMBER OF OPERANDS
0038 171 : THE INSTRUCTION NEEDS. STARTING AT BYTE 'SYMSK_BLKSIZ' IS A
0038 172 : STRING OF BYTES DESCRIBING THE OPERANDS. THE LOW 4 BITS DEFINE
0038 173 : THE SIZE OF THE OPERAND, THE NEXT 3 BITS ARE AN INDEX INTO THE
0038 174 : ILLEGAL MODE TABLE, AND THE LAST BIT IS SET IF IT IS A FLOATING
0038 175 : OPERAND.
0038 176 :
0038 177 :--
0038 178 :
00000000 179 .PSECT MAC\$RO_CODE_P1,NOWRT,GBL,LONG
0000 180
0000 181 STAT1:: :STATEMENT = MACHINE_STAT
0000 182
0000'CF D5 0000 183 TSTL W^MAC\$GL_MOPNUM :WERE THERE ENOUGH OPERANDS?
08 15 0004 184 BLEQ 10\$:IF LEQ YES
0006 185 SMAC_ERR NOTENUOPR :No--set message code
FFF2' 30 0008 186 BSBW MAC\$ERRORPT :SEND ERROR MSG TO INT. FILE
05 000E 187 10\$: RSB

000F 189 .SBTTL OPCODE GENERATION
 000F 190
 000F 191 ++
 000F 192 : FUNCTIONAL DESCRIPTION:
 000F 193
 000F 194 MINST1 IS INVOKED WHEN AN OPCODE IS ENCOUNTERED. IT SETS
 000F 195 UP TO PROCESS THE OPERANDS THAT FOLLOW THE OPCODE.
 000F 196
 000F 197 : INPUTS:
 000F 198
 000F 199 MAC\$GL_VALUE SYMBOL BLOCK ADDRESS OF OPCODE
 000F 200
 000F 201 : OUTPUTS:
 000F 202
 000F 203 MAC\$GL_MOPNUM NUMBER OF OPERANDS FOR THIS OPCODE
 000F 204 MAC\$GL_MOPPTR POINTER TO OPERAND WORD DESCRIPTORS
 000F 205
 000F 206 --
 000F 207
 000F 208 MINST1:: ;MACHINE_INST = DOPCODE
 000F 209
 56 0000'CF FFE9' D0 000F 210 MOVL W^MAC\$GL_VALUE,R6 :GET SYMBOL BLOCK ADDRESS
 30 0014 211 BSBW MAC\$CREF_OPCODE :CREF THE OPCODE IF NEEDED
 0017 212 \$INTOUT_WD INTS_OP,SYMSL_VAL(R6) :OUTPUT OPCODE TO PASS 2
 0021 213 \$INC_PC :UPDATE PC FOR OPCODE
 06 A6 95 0025 214 TSTB SYMSL_VAL+1(R6) :TWO-BYTE OPCODE?
 04 13 0028 215 BEQL 10\$:IF EQL NO
 002A 216 \$INC_PC :YES--UPDATE PC FOR 2-BYTE OPCODE
 0C A6 9A 002E 217 10\$: MOVZBL SYMSB_SEG(R6) - :SET UP OPERAND COUNTER
 0000'CF 0031 218 W^MAC\$GL_MOPNUM
 0D A6 9E 0034 219 MOVAB SYMSK_BLKSIZ(R6) - :POINT TO OPERAND MODE WORD DESCRIPTORS
 0000'CF 0037 220 W^MAC\$GL_MOPPTR :
 003A 221
 003A 222 : EXIT FROM MACHINE INSTRUCTION OR OPERAND--SET FOR NEXT OPERAND
 003A 223
 003A 224 :
 003A 225
 003A 226 MACH_OP_EXIT:
 04 AB 1010 8F AA 003A 227 : Clear Index Mode de-optimize flag.
 06 6B 14 E5 003A 228 bicw2 #FLGSM_UPAFLG!FLGSM_OPTVFLIDX,4(r11) ; and DUpA flag.
 0040 229 BBCC #FLGSV_CHKLPND,(R11),5\$:CHKL PENDING?
 0044 230 \$INTOUT_X INTS_CHKL :YES--SEND IT NOW
 50 05 00 EF 004A 231 5\$: EXTZV #OPDSV_SIZE,#OPDSS_SIZE,- ;GET SIZE OF OPERAND
 0000'CF 004D 232 W^MAC\$GL_MOPPTR,R0
 50 0000'DF 0051 233 MOVL R0,W^MAC\$GL_OPSIZE :AND STORE FOR LATER USE
 0000'CF D0 0051 234 10\$: CLRL W^MAC\$GB_MODE :CLEAR MODE,IMODE,REG, AND IREG
 0000'CF D4 0056 235 MOVL W^MAC\$GL_PSECT,- :START WITH CURRENT PSECT
 0000'CF D0 005A 236 W^MAC\$GL_PRMSEG :
 0000'CF 59 D1 0061 237 CMPL R9,W^MAC\$GL_INTWRNPT :NEAR THE END OF THE INT. BUFFER?
 03 1B 0066 238 BLEQU 20\$:IF LEQU NO
 FF95' 30 0068 239 BSBW MAC\$OUTFRAME :YES--SET UP FOR NEW BUFFER
 0000'CF 59 D0 006B 240 20\$: MOVL R9,W^MAC\$GL_EXPPTR :SAVE PTR TO EXPRESSION START
 0000'CF 59 D0 0070 241 MOVL R9,W^MAC\$GL_EXPEND :AND EXPRESSION END
 000000C4 8F C8 0075 242 BISL2 #FLGSM_COMPEXPR!FLGSM_EXPPT!FLGSM_EVALEXPR,-
 6B 007B 243 (R11) :ASSUME COMPILE TIME EXPRESSION.
 007C 244 : ALLOW EXPRESSION OPTIMIZATION
 007C 245 : AND EVALUATE ON PASS 2

0000'CF	0000'CF	D4	007C	246	CLRL	W^MAC\$GL_ABSFLAG	ASSUME ABSOLUTE EXPRESSION
0000'CF	0000'CF	D0	0080	247	MOVL	W^MAC\$GL_PC	W^MAC\$GL_SAVEPC ;SAVE PC FOR ERROR RECOVERY
0000'CF	0000'CF	D4	0087	248	CLRL	W^MAC\$GL_HIGH_32	;CLEAR HI 32 BITS IN CASE QUAD OPERAND
		05	008B	249	RSB		

008C 251

.SBTTL OPERAND GENERATION

008C 252

++ FUNCTIONAL DESCRIPTION:

008C 253

008C 254

008C 255

008C 256

008C 257

008C 258

008C 259

008C 260

008C 261

008C 262

008C 263

008C 264

008C 265

008C 266

008C 267

008C 268

008C 269

008C 270

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008C 274

008C 275

0000'CF 0000'CF

11 14

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009A 009

00A1 00A

00A3 00A

00A8 00A

00AB 00A

00AD 00A

00B2 00B

00BA 00B

00BE 00B

00C3 00C

00C6 00C

00C8 00C

00CB 00C

00CD 00C

00D0 00D

00D2 00D

00D7 00D

00D8 00D

00D9 00D

00D0 00D

00E1 00E

00E4 00E

00E6 00E

00E8 00E

00EC 00E

00F0 00F

00F4 00F

00F8 00F

00F9 00F

00FA 00F

00FB 00F

00FC 00F

00FD 00F

00FE 00F

00FF 00F

0000'CF 0000'CF

04 68

02 E0

00EC

305 305

306 306

307 307

408 408

FUNCTIONAL DESCRIPTION:

OPRAND IS INVOKED WHEN A REFERENCE (OPRAND) HAS BEEN SCANNED.
 IF THERE ARE TOO MANY OPERANDS A MESSAGE IS ISSUED TO PASS 2.
 THE MODE OF THE REFERENCE IS CHECKED TO SEE IF IT IS LEGAL FOR
 THIS OPERAND. THE REFERENCE IS THEN EMITTED TO PASS 2.

INPUTS:

MAC\$GL_MOPPTR
MAC\$GB_MODE
POINTER TO OPERAND WORD DESCRIPTOR
MODE OF OPERAND

OUTPUTS:

THE INTERMEDIATE CODE FOR THIS OPERAND IS EMITTED TO THE
INTERMEDIATE FILE.

OPRAND::

:OPERANDS = REF
:OPERANDS = OPERANDS DCOMMA REF

TSTL W^MAC\$GL_MOPNUM
BGTR 10S
\$MAC_ERR TOOMNYOPND
BSBW MAC\$ERRORPX
MOVL W^MAC\$GL_SAVE_PC,W^MAC\$GL_PC ;RESET PC TO NOT COUNT OPERAND
BRB MACH_OP_EXIT
MOVZWL #OPDSV_MODE,#OPD\$S_MODE,- ;GET OPERAND DESC. WORD THIS OPRAND
EXTZV R6,R5 ;GET THE OPERAND MODE
MOVZBL W^MAC\$GB_MODE,R4 ;GET OPERAND MODE WE SCANNED
MOVZWL L^MACSAW_ILLMODTB[R5],R0 ;GET TABLE ENTRY FOR ACCESS MODE
BBC R4,R0,20S ;BRANCH IF LEGAL MODE
\$MAC_ERR ILLMODE ;No--get message code
CMPB R4,#ADM\$REGISTER ;Is addressing mode register?
BNEQ 14\$;No if NEQ
BSBW MAC\$ERRORPX ;SEND ERROR TO PASS 2
BRB 16S
14\$: ;Send error to pass-2
BSBW MAC\$ERRORPT
CLRL R6
ADDL2 #2,W^MAC\$GL_MOPPTR ;USE ZERO DESCRIPTOR
DECL W^MAC\$GL_MOPNUM ;ADVANCE TO NEXT DESCRIPTOR
BNEQ 30S ;DECREMENT OPERAND COUNT
CMPW W^MAC\$GL_ERRPTX,- ;IF NEQ THEN NOT LAST OPERAND
#MAC\$AB_LINEBF ;LAST OPERAND--FIRST ON LINE?
BEQL 30S ;IF EQL YES
BBCS #OPF\$V_LASTOPR,- ;NO--MARK LAST OPERAND
BBCS W^MAC\$GL_OPSIZE 30S ;
BBS #FLGSV_COMPEXPR(R11) 40\$;BRANCH IF OPTIMIZABLE
BBC #FLGSV_EXPOTP,(R11),50\$;ELSE FLAG UNABLE TO OPTIMIZE
BBC #FLGSV_EXPOTP,(R11),50\$;BRANCH IF UNABLE TO OPTIMIZE

	FF05'	30	00F8	308	BSBW	MACSOPTIMIZEEXPR	:OPTIMIZE EXPRESSION			
	0C	E3	00FB	309	BBCS	#OPFSV_OPTEXP -	:MARK OPTIMIZED			
00	0000'CF		00FD	310		W^MAC\$GL_OPSIZE,	50\$			
00A1	8F	56	B1	0101	50\$:	CMPW	R6 #OPDSM_BB			
	OE	13	0106	311	BEQL	60\$:BRANCH DESTINATION?			
00C2	8F	56	B1	0108	312	CMPW	R6 #OPDSM_BW			
	03	13	010D	313	BEQL	55\$:BRANCH DESTINATION?			
	008E	31	010F	314	BRW	120\$:IF EQL YES			
				315	\$INC_PC		:ELSE NOT A BRANCH DESTINATION			
OF	0000'CF	91	0116	316	55\$:	CMFB	W^MAC\$GB_VAL3,#REGS_PC	:YES--UPDATE PC FOR BRANCH WORD		
	0B	13	011B	317	60\$:	BEQL	70\$:REGISTER MUST BE 'PC'		
	FEDB'	30	0122	318	\$MAC_ERR	ILLBRDEST	:IF EQL OK			
	00F4	31	0125	319	BSBW	MACSERRORPX	:Illegal branch destination			
0A	0000'CF	91	0128	320	BRW	150\$:SEND ERROR TO PASS 2			
	06	12	012D	321	CMPB	W^MAC\$GB_MODE,#ADMS_BYTE_DISP	:FINISH			
				322	BNEQ	80\$:CORRECT BRANCH SIZE			
				323	\$DEC_PC					
OC	0000'CF	13	0133	324	BRB	100\$:JOIN COMMON CODE			
	07	91	0135	325	CMPB	W^MAC\$GB_MODE,#ADMS_WORD_DISP				
		12	013A	326	BNEQ	90\$				
		05	013C	327	\$DEC_PC	#2				
		05	0141	328	BRB	100\$				
			0143	329	\$DEC_PC	#4				
S3	0000'CF	7E	94	0148	330	CLRB	-(SP)	:ASSUME NOT OPTIMIZED		
37	6B	07	D0	014A	331	MOVL	W^MAC\$GL_EXPPOVL1,R3	:GET (MAYBE) OPTIMIZED VALUE		
			E0	014F	332	BBS	#FLGSV_EXPPOPT,(R11),110\$:BRANCH IF WE OPTIMIZED		
		53	D4	0153	333	CLRL	R3	:ASSUME GLOBAL		
50	52	0000'CF	D0	0155	334	MOVL	W^MAC\$GL_EXPPT,R2	:GET EXPRESSION POINTER		
		52	C3	015A	335	SUBL3	R2,W^MAC\$GL_EXPEND,RO	:COMPUTE SIZE OF EXPRESSION		
				0160	336					
				0160	337	104\$:				
		28	13	0160	338	BEQL	110\$			
	06	50	D1	0162	339	CMPL	RO,#6	:IF EQL NO EXPRESSION		
		11	13	0165	340	BEQL	106\$:6 BYTES?		
17	01	A2	91	0167	341	CMPB	1(R2),#INTS_NEWL	:Yes if EQL		
		1D	12	016B	342	BNEQ	110\$:Is it a new-line?		
	51	62	9A	016D	343	MOVZBL	(R2),R1	:No if NEQ		
	52	51	C0	0170	344	ADDL2	R1,R2	:Get frame length		
	50	51	C2	0173	345	SUBL2	R1,RO	:Point to next frame		
		E8	11	0176	346	BRB	104\$:and reduce size of expression		
				0178	347	106\$:				
2D	01	A2	91	0178	348	CMPB	1(R2),#INTS_STKS	:YES--STACK SYMBOL REFERENCE?		
		0C	12	017C	349	BNEQ	110\$:IF NEQ NO		
53	02	A2	D0	017E	350	MOVL	2(R2),R3	:YES--GET ID ADDRESS		
6E	0000'CF	90	0182	351	MOVB	W^MAC\$GL_PSECT,(SP)	:MUST BE IN SAME PSECT			
	02	A2	D4	0187	352	JRL	2(R2)	:FLAG SPECIAL RESOLUTION		
	50	09	9A	018A	353	MOVZBL	#9,RO	:WE WILL OUTPUT 9 BYTES		
		FE70'	30	018D	354	BSBW	MAC\$INTOUT_N	:MAKE ROOM FOR THEM		
	89	0E	90	0190	355	MOVB	#INTS_BDST,(R9)+	:STORE INT. CODE		
89	0000'CF	B0	0193	356	MOVW	W^MAC\$GL_OPSIZE,(R9)+	:STORE FLAGS			
		53	D0	0198	357	MOVL	R3,(R9)+	:STORE 0 OR SYMBOL ID ADDRESS		
	89	8E	90	019B	358	MOVB	(SP)+(R9)+	:STORE 0 OR PSECT NUMBER		
		7C	11	019E	359	BRB	150\$			
				01A0	360	:				
				01A0	361	: NOT BRANCH DESTINATION				
				01A0	362	:				
OF	14	6B	24	E1	01A0	363	120\$:	BBC	#FLGSV_UPAFLG,(R11),125\$:BRANCH IF DUPA WAS NOT SEEN
	0000'CF	91	01A4	364	CMPB	W^MAC\$GB_REG,#REGS_PC	:YES--IS REGISTER PC?			

0A	0D	12	01A9	365		BNEQ	125\$: IF NEQ NO	
	54	91	01AB	366		CMPB	R4 #ADMS_BYTE_DISP	: YES--IS MODE LEGAL?	
	08	19	01AE	367		BLSS	125\$: IF LSS YES	
			01B0	368		SMAC_ERR	OPRNDNSYNX	: NO--TELL OF OPERAND SYNTAX ERROR	
1A	6B FE48'	30	01B5	369	125\$:	BSBW	MAC\$ERRORPT		
	07	E1	01B8	370		BBC	#FLGSV_EXPOPT,(R11),130\$: BRANCH IF CANNOT OPTIMIZE	
	50	0C	9A	01BC		MOVZBL	#12, R0	: SET TO STORE 12 BYTES	
	FE3E'	30	01BF	372		BSBW	MAC\$INTOUT_N	: SET UP FOR IT	
89	89 1E	90	01C2	373		MOVB	#INTS REF,(R9)+	: STORE INT. CODE	
	0000'CF	DO	01C5	374		MOVL	W^MAC\$GL_VALUE,(R9)+	: STORE REGISTERS/MODES	
89	0000'CF	80	01CA	375		MOVW	W^MAC\$GL_OPSIZE,(R9)+	: STORE FLAGS	
89	0000'CF	DO	01CF	376		MOVL	W^MAC\$GL_EXPOVVL1,(R9)+	: STORE OPTIMIZED VALUE	
	13	11	01D4	377		BRB	140\$		
	50	08	9A	01D6	378	130\$:	MOVZBL	#8, R0	: SET TO STORE 8 BYTES
	FE24'	30	01D9	379		BSBW	MAC\$INTOUT_N	: SET UP FOR IT	
89	89 1E	90	01DC	380		MOVB	#INTS REF,(R9)+	: STORE INT. CODE	
89	0000'CF	DO	01DF	381		MOVL	W^MAC\$GL_VALUE,(R9)+	: STORE MODES/REGISTERS	
89	0000'CF	80	01E4	382		MOVW	W^MAC\$GL_OPSIZE,(R9)+	: STORE FLAGS	
			01E9	383	140\$:				
01	0000'CF	91	01E9	384		CMPB	W^MAC\$GL_VALUE,#ADMS_IMMEDIATE	: Is address mode immediate?	
	2C	12	01EE	385		BNEQ	150\$: No if NEQ	
08	0000'CF	91	01F0	386		CMPB	W^MAC\$GL_OPSIZE,#8	: Is operand a QUAD or OCTA value?	
	25	19	01F5	387		BLSS	150\$: No if LSS	
10	0000'CF		01F7	388		SINTOUT	LW INTS STIL,<W^MAC\$GL_HIGH_32>	: Output bits 32-63	
	14	91	0201	389		CMPB	W^MAC\$GE_OPSIZE,#16	: Is operand an OCTA value?	
	12	0206	390			BNEQ	150\$: No if NEQ	
		0208	391			SINTOUT	LW INTS STIL,<W^MAC\$GQ_HIGH_64+0>	: Output bits 64-95	
		0212	392			SINTOUT	LW INTS STIL,<W^MAC\$GQ_HIGH_64+4>	: and then bits 96-127	
0000'CF	02	90	021C	393	150\$:	MOVB	#RDXSV_DECIMAL,W^MAC\$GB_RDXNDX	: RESET RADIX	
	FE16	31	0221	394		BRW	MACH_OP_EXIT		

0224 396 .SBTTL ASSIGNMENT STATEMENTS
 0224 397
 0224 398 :++
 0224 399 : FUNCTIONAL DESCRIPTION:
 0224 400 :
 0224 401 : THESE ROUTINES ARE INVOKED WHEN AN ASSIGNMENT STATEMENT
 0224 402 : IS DETECTED. IF ENTRY AT ASSHD3, IT IS FLAGGED AS AN
 0224 403 : ASSIGNMENT TO 'PC'. IF ENTRY AT ASSHD2, THE SYMBOL
 0224 404 : IS FLAGGED AS GLOBAL.
 0224 405 :
 0224 406 : INPUTS:
 0224 407 :
 0224 408 : MAC\$AL_VALSTACK-8[R7] (ASSHD2) SYMBOL BLOCK OF ID
 0224 409 : MAC\$AL_VALSTACK-4[R7] (ASSHD1) SYMBOL BLOCK OF ID
 0224 410 :
 0224 411 : OUTPUTS:
 0224 412 :
 0224 413 : MAC\$GL_ASNPTR
 0224 414 : MAC\$GL_OPSIZE
 0224 415 :
 0224 416 :--
 0224 417 :
 0224 418 :
 50 FF 8F 98 0224 419 ASSHD3:: :ASSIGN HEAD = DPC
 18 11 0224 420 CVTBL :MARK PC AUGMENTATION
 0228 BRB #1, R0
 022A ASSIGN_HEAD
 022A 421 BRB
 022A 422 ASSHD2:: :ASSIGN HEAD = ID DEQ DEA
 50 FFF8'CF47 022A 423 MOVL W^MAC\$AL_VALSTACK-8[R7], R0 ;POINT TO ID SYMBOL BLOCK
 09 A0 04 0230 424 BISW2 #SYMSM_G[OBL, SYMSW_FLAG(R0) ;MARK SYMBOL AS GLOBAL
 09 A0 0800 8F 0234 425 BISW2 #SYMSM_RELPSCT, SYMSW_FLAG(R0) ;ALWAYS OUTPUT GLOBAL SYMBOL
 06 11 023A 426 BRB
 023C 427 ASSIGN_HEAD
 023C 428
 50 FFFC'CF47 023C 429 ASSHD1:: :ASSIGN HEAD = ID DEQ
 0000'CF 50 0242 430 MOVL W^MAC\$AL_VALSTACK-4[R7], R0 ;POINT TO ID SYMBOL BLOCK
 0000'CF 0242 431 ASSIGN_HEAD:
 00 6B 06 0247 432 MOVL R0, W^MAC\$GL_ASNPTR :SAVE POINTER TO ID
 00002004 8F C8 0248 433 CLRL W^MAC\$GL_PRMSEG :ALLOW EXPRESSION IN ANY SEGMENT
 6B 024F 434 BBCC #FLGSV_EVALEXPR,(R11),10\$;DON'T EVALUATE EXPRESSION
 0000'CF D4 0256 435 10\$: BISL2 #FLGSM_COMPEXPR!FLGSM_OPRND,- ;ASSUME COMPILE TIME EXPR
 0000'CF 04 9A 025A 436 CLRL (R11) :AND FLAG IN OPERAND FIELD
 025F 437 W^MAC\$GL_ABSFLAG :ASSUME ABSOLUTE EXPRESSION
 025F 438 MOVZBL #4, W^MAC\$GL_OPSIZE :SET OPERAND MAX SIZE TO 4 BYTES
 025F 439 :
 025F 440 : IF CREFFING, SAVE LINE/PAGE SO THEY ARE CORRECT
 025F 441 :
 21 6B 1E E1 025F 442 BBC #FLGSV_CRF,(R11),30\$:BRANCH IF NOT CREFFING
 0000'CF D0 0263 443 MOVL W^MAC\$GL_SRCPAG,- :YES--SAVE SOURCE PAGE
 0000'CF 0267 444 W^MAC\$GL_SAV_PAG
 0000'CF D0 026A 445 MOVL W^MAC\$GL_LINBAS,- :SAVE LINE BASE
 0000'CF 026E 446 W^MAC\$GL_SAV_BAS
 50 0000'CF D0 0271 447 MOVL W^MAC\$GL_LINENUM, R0 :GET THE LINE NUMBER
 05 6B 19 E1 0276 448 BBC #FLGSV_SEQFIL,(R11),20\$:BRANCH IF NOT SEQUENCED
 50 0000'CF D0 027A 449 MOVL W^MAC\$GL_RECHDBUF, R0 :YES--GET SEQUENCE NUMBER
 0000'CF 50 D0 027F 450 20\$: MOVL R0, W^MAC\$GL_SAV_LIN :AND SAVE LINE NUMBER
 05 0284 451 30\$: RSB

0285 453 ;++
 0285 454 : FUNCTIONAL DESCRIPTION:
 0285 455
 0285 456 : ASSGN1 IS INVOKED TO FINISH PROCESSING AN ASSIGNMENT STATEMENT.
 0285 457 : THE EXPRESSION HAS BEEN EVALUATED, AND IS ON THE VALUE STACK.
 0285 458 : IF THE ASSIGNMENT IS TO THE PC, CODE IS EMITTED TO THE INTERMEDIATE
 0285 459 : FILE TO AUGMENT THE PC. IF THE ASSIGNMENT IS NOT TO PC, A
 0285 460 : CHECK IS MADE FOR A MULTIPLE LABEL DEFINITION, AND THEN THE
 0285 461 : FLAGS IN THE SYMBOL BLOCK ARE UPDATED. CODE IS EMITTED TO
 0285 462 : THE INTERMEDIATE FILE TO UPDATE THE SYMBOL BLOCK IN PASS 2.
 0285 463
 0285 464 : INPUTS:
 0285 465
 0285 466 : MACSGL ASN PTR (-1) IF PC AUGMENTATION, ELSE POINTER
 0285 467 : TO SYMBOL BLOCK OF ID.
 0285 468 : MACSAL_VALSTACK-4[R7] EXPRESSION VALUE
 0285 469
 0285 470 : OUTPUTS:
 0285 471
 0285 472
 0285 473 :--
 0285 474
 0285 475 ASSGN1::

56 0000'CF 08 6B 02 D0 0285 476 MOVL W^MACSGL ASN PTR, R6 ;ASSIGNMENT = ASSIGN HEAD EXPR DEOL
 0285 477 BBS #FLGSV COMP(EXPR,(R11),10\$;GET POINTER TO ID SYMBOL BLOCK
 0285 478 SMAC_ERR ASGNMNTSYN ;MUST BE COMPILE TIME EXPRESSION
 0285 479 BSBW MACSERRORT ;No--send message to pass 2
 50 56 FD6A' 01 22 30 0293 480 10\$: ADDL3 #1 R6, R0 ;IS THIS PC ASSIGNMENT (R6=-1)?
 0285 481 BNEQ 20\$;IF NEQ NO
 0285 482 BSBW MACSSET PC ;YES--RECORD HI MARK OF PC
 55 56 FFFC'CF47 0000'CF 008D 0285 483 MOVL W^MACSAL_VALSTACK-4[R7], R6 ;GET NEW VALUE
 0285 484 SUBL3 W^MACSGL_PC, R6, R5 ;COMPUTE AUGMENTATION
 0285 485 SINTOUT_LW INTS AUGPC, R5 ;SEND TO PASS 2
 0285 486 MOVL R6, W^MACSGL_PC ;SET NEW PC
 0285 487 BSBW MACSSET_PC ;CHECK NEW PC
 0285 488 BRW B0\$
 0285 489 :
 0285 490 : EXPRESSION DOES NOT INVOLVE PC
 0285 491
 08 09 A6 03 E1 0285 492 20\$: BBC #SYMSV EXTRN, SYMSW_FLAG(R6), 30\$;EXTERNAL?
 0285 493 SMAC_ERR SYMDCEEXTR ;Yes-error
 0285 494 BSBW MACSERRORT ;ISSUE ERROR TO PASS 2
 0285 495 30\$: BSBW MACSMUL DEF CHK ;SEE IF MULTIPLY DEFINED
 0285 496 MOVB W^MACSGL_PRSEG, SYMSB SEG(R6) ;DEFINE IN EXPRESSION PSECT
 0285 497 BICW2 #SYMSM ABS, SYMSW_FLAG(R6) ;ASSUME NOT ABSOLUTE
 0285 498 TSTL W^MACSGL_ABSFLAG ;IS EXPRESSION ABSOLUTE?
 0285 499 BNEQ 50\$;IF NEQ NO
 0285 500 CLR B SYMSB SEG(R6) ;YES--MAKE ABSOLUTE PSECT
 00 09 A6 04 E3 02E1 501 BBCS #SYMSV_ABS, SYMSW_FLAG(R6), 50\$;SET ABSOLUTE FLAG
 0285 502 50\$: BBS #SYMSV_LOCAL, SYMSW_FLAG(R6), 60\$;IS SYMBOL LOCAL?
 0285 503 BLBC W^ENBSG DEBUG+SYMSV VAL, 60\$;NO--BRANCH IF NO ENABLE DEBUG
 0285 504 BISW2 #SYMSM DEBUG, SYMSW_FLAG(R6) ;LET DEBUGGER KNOW ABOUT SYMBOL
 0285 505 MOVL W^MACSAL_VALSTACK-4[R7], - ;PUT IN SYMBOL VALUE
 0285 506 SYMSL VAL(R6)
 0101 8F A8 02FB 507 BISW2 #SYMSM_DEF!SYMSM ASN - ;MARK AS DEFINED BY ASSIGNMENT
 0285 508 SYMSW_FLAG(R6)
 55 00'BF 9A 0301 509 MOVZBL #CRFSK_DEF, R5 ;SET DEFINITION FLAG

0000'CF	DD	0305	510	PUSHL	W^MACSGL_LINBAS	:GET READY TO SET RIGHT LINE/PAGE
0000'CF	DD	0309	511	PUSHL	W^MACSGL_LINENUM	:BY SAVING CURRENT VALUES
0000'CF	DD	030D	512	PUSHL	W^MACSGL_SRCPAG	:....
0000'CF	DD	0311	513	PUSHL	W^MACSGL_RECHDBUF	
0000'CF	DD	0315	514	MOVL	W^MACSGL_SAV_BAS,-	:NOW SET VALUES WE WANT
0000'CF	DD	0319	515		W^MACSGL_LINBAS	
50 0000'CF	DO	031C	516	MOVL	W^MACSGL_SAV_LIN,RO	
0000'CF 50	DO	0321	517	MOVL	RO,W^MACSGL_LINENUM	
0000'CF 50	DO	0326	518	MOVL	RO,W^MACSGL_RECHDBUF	: (IN CASE SEQUENCED FILE)
0000'CF	DO	0328	519	MOVL	W^MACSGL_SAV_PAG,-	
0000'CF		032F	520		W^MACSGL_SRCPAG	
FCCB' 30		0332	521	BSBW	MACSCREF_SYM	:OUTPUT TO CREF IF CREFING
0000'CF 8ED0		0335	522	POPL	W^MACSGL_RECHDBUF	:RESTORE OLD LINES/PAGES
0000'CF 8ED0		033A	523	POPL	W^MACSGL_SRCPAG	
0000'CF 8ED0		033F	524	POPL	W^MACSGL_LINENUM	
0000'CF 8ED0		0344	525	POPL	W^MACSGL_LINBAS	
10 10		0349	526	BSBB	MACSINTOUT ASN	:OUTPUT ASN TO INTERMED. FILE
00 6B 06	E3	034B	527	BBCS	#FLG\$V_EVA[EXPR,(R11),90\$:ALLOW EXPRESSION EVALUATION
		034F	528		\$INTOUT_LW INT\$_PRIL,<W^MACSAL_VAL\$STACK-4[R7]>	:PRINT EXPRESSION
		035A	529	RSB		

035B 531 :++
 035B 532 : FUNCTIONAL DESCRIPTION:
 035B 533 :
 035B 534 : THIS ROUTINE OUTPUTS AN ASSIGN COMMAND AND DATA TO THE
 035B 535 : INTERMEDIATE FILE.
 035B 536 :
 035B 537 : INPUTS:
 035B 538 :
 035B 539 : R6 POINTS TO SYMBOL BLOCK
 035B 540 :
 035B 541 :--
 035B 542 :
 035B 543 : MAC\$INTOUT ASN::
 50 0C 9A 035B 544 MOVZBL #12, R0 :SIZE OF AN ASN COMMAND AND DATA
 89 FC9F 30 035E 545 BSBW MAC\$INTOUT N :MAKE ROOM FOR IT
 89 0C 90 0361 546 MOVB #INT\$ ASN,(R9)+ :STORE THE COMMAND
 89 56 D0 0364 547 MOVL R6,(R9)+ :STORE SYMBOL BLOCK ADDRESS
 89 0C A6 90 0367 548 MOVB SYMSB_SEG(R6),(R9)+ :STORE SYMBOL SEGMENT
 89 05 A6 D0 036B 549 MOVL SYMSL_VAL(R6),(R9)+ :STORE SYMBOL VALUE
 02 09 A6 04 E0 0371 550 CLRL R0 :ASSUME ABSOLUTE
 50 50 D6 0376 551 BBS #SYMSV_ABS,SYMSW_FLAG(R6),10\$;BRANCH IF ABSOLUTE
 89 50 90 0378 552 INCL R0 :NO--MAKE RELOCATABLE
 05 037B 553 10\$: MOVBL R0,(R9)+ :STORE ABS/REL FLAG
 037C 554 RSB :
 037C 555 :
 037C 556 :++
 037C 557 : FUNCTIONAL DESCRIPTION:
 037C 558 :
 037C 559 : THIS ROUTINE CHECKS FOR A MULTIPLY DEFINED LABEL. IF THE
 037C 560 : LABEL IS MULTIPLY DEFINED, AN ERROR MESSAGE IS ISSUED TO
 037C 561 : PASS 2.
 037C 562 :
 037C 563 : INPUTS:
 037C 564 :
 037C 565 : R6 SYMBOL BLOCK ADDRESS
 037C 566 :
 037C 567 :--
 037C 568 :
 037C 569 : MAC\$MUL_DEF_CHK::
 0D 09 A6 00 E1 037C 570 BBC #SYMSV_DEF,SYMSW_FLAG(R6),10\$;BRANCH IF NOT DEFINED
 08 09 A6 08 E0 0381 571 BBS #SYMSV ASN,SYMSW_FLAG(R6),10\$;BRANCH IF BY ASSIGNMENT
 FC72' 31 0386 572 SMAC_ERR_MULDEFLBL : This is multiply defined
 06 0005'CF E9 038E 573 BRW MAC\$ERRORPT : ISSUE ERROR TO PASS 2
 09 A6 4000 BF A8 0393 574 10\$: BLBC W\$ENBSG_SUPPRESS+SYMSL_VAL,20\$;BRANCH IF NOT ENABLE SUPPRESSION
 05 0399 575 BISW2 #SYMSM_SUPR,SYMSW_FLAG(R6) ;YES-SET SUPPRESS BIT THIS SYMBOL
 576 20\$: RSB :

039A 578 .SBTTL BLOCK DATA STORAGE DIRECTIVES
 039A 579
 039A 580 ++
 039A 581 : FUNCTIONAL DESCRIPTION:
 039A 582
 039A 583 THESE ROUTINES (BLKBYT, BLKWRD, BLKLNG, BLKQUD AND BLKOCT) ARE
 039A 584 CALLED WHEN A BLOCK DATA DIRECTIVE IS SCANNED. THE
 039A 585 SHIFT COUNT FOR THE PARTICULAR DATA TYPE IS SET INTO
 039A 586 MAC\$GL VALUE AND FLAGS ARE SET TO SCAN THE EXPRESSION
 039A 587 INDICATED THE NUMBER OF UNITS OF STORAGE TO ALLOCATE.
 039A 588
 039A 589 : OUTPUTS:
 039A 590
 039A 591 MAC\$GL_VALUE SHIFT COUNT TO SHIFT NUMBER OF UNITS INTO BYTES
 039A 592 MAC\$GL_FLAGS FLGSM_COMPEXPR IS SET (LOOK FOR COMPILE TIME EXPRESSION
 039A 593 FLGSM_EVALEXPR IS CLEARED (DON'T EVALUATE ON PASS 2)
 039A 594 --
 039A 595
 039A 596 .ENABL LSB
 039A 597
 039A 598 039A 599 BLKBYT:: ;BLOCK_TYPE = KBLKB
 0D 10 039A 600 BSBB 10\$;GO TO COMMON ROUTINE
 00 00 039C 601 .BYTE 0 ;SHIFT ALLOCATION 0
 039D 602
 039D 603 BLKWRD:: ;BLOCK_TYPE = KBLKW
 0A 10 039D 604 BSBB 10\$;GO TO COMMON ROUTINE
 01 039F 605 .BYTE 1 ;SHIFT ALLOCATION ONCE
 03A0 606
 03A0 607 BLKLNG:: ;BLOCK_TYPE = KBLKL
 03A0 608 ; OR KLBKA
 03A0 609 ; OR KBLKF
 07 10 03A0 610 BSBB 10\$;GO TO COMMON ROUTINE
 02 03A2 611 .BYTE 2 ;SHIFT ALLOCATION TWICE
 03A3 612
 03A3 613 BLKQUD:: ;BLOCK_TYPE = KBLQ
 03A3 614 ; OR KBLKD
 03A3 615 ; OR KBLKG
 04 10 03A3 616 BSBB 10\$;GO TO COMMON ROUTINE
 03 03A5 617 .BYTE 3 ;SHIFT ALLOCATION THREE TIMES
 03A6 618
 03A6 619 BLKOCT:: ;BLOCK_TYPE = KBLKO
 03A6 620 ; OR KBLKH
 01 10 03A6 621 BSBB 10\$;GOTO COMMON ROUTINE
 04 03A8 622 .BYTE 4 ;SHIFT ALLOCATION FOUR TIMES
 03A9 623
 0000'CF 9E 9A 03A9 624 10\$: MOVZBL A(SP)+,W\$MAC\$GL_VALUE ;SET SHIFT COUNT AS VALUE
 6B 04 C8 03AE 625 BISL2 #FLGSM_COMPEXPR,(R11) ;LOOK FOR COMPILE TIME EXPRESSION
 00 6B 06 E5 03B1 626 BBCC #FLGSV_EVALEXPR,(R11),..+1 ;DON'T OUTPUT EXPRESSION TO PASS 2
 0000'CF D4 03B5 627 CLRL W\$MAC\$GL_ABSFLAG ;LOOK FOR ABSOLUTE EXPRESSION
 05 03B9 628 RSB
 03BA 629
 03BA 630 .DSABL LSB

03BA 632 :++
 03BA 633 : FUNCTIONAL DESCRIPTION:
 03BA 634 :
 03BA 635 : THESE TWO ROUTINES (BSTAT1 AND BSTAT2) FINISH PROCESSING OF
 03BA 636 : BLOCK DATA DIRECTIVES.
 03BA 637 :--
 03BA 638 :
 03BA 639 :
 03BA 640 .ENABL LSB
 03BA 641 :
 03BA 642 BSTAT1:: :BLOCK STAT = BLOCK_TYPE
 55 0000'CF47 01 9A 03BA 643 MOVZBL #1,R6 :DEFAULT ALLOCATION IS 1 UNIT
 0C 00 03BD 644 MOVL W^MACSAL_VALSTACK[R7],R5;GET THE SHIFT COUNT
 11 03C3 645 BRB 10\$:
 03C5 646 :
 03C5 647 BSTAT2:: :BLOCK STAT = BLOCK_TYPE EXPR
 56 0000'CF47 00 03C5 648 MOVL W^MACSAL_VALSTACK[R7],R6;GET NUMBER OF UNITS OF ALLOCATION
 55 FFFC'CF47 00 03C8 649 MOVL W^MACSAL_VALSTACK-4[R7],R5;GET THE SHIFT COUNT
 FC2C' 30 03D1 650 10\$: BSBW MACSSET PC :RECORD HIGH PC
 0000'CF 0A 13 03D4 651 TSTL W^MACSGE_ABSFLAG :EXPRESSION MUST BE ABSOLUTE
 FC1E' 30 03D8 652 BEQL 20\$:IF EQL IT IS
 56 56 55 7C 03DA 653 SMAC_ERR BLKEXPNAbs :Set message code
 55 7C 03E2 654 BSBW MACSErrorRPT :ISSUE MESSAGE TO PASS 2
 03E4 655 CLRQ R5 :ALLOCATE NO SPACE
 03E8 656 20\$: ASHL R5,R6,R6 :CONVERT ALLOCATION TO BYTES
 03F0 657 \$INTOUT_LW INT\$_AUGPC,R6 :AUGMENT PC BY THAT MANY BYTES
 03F5 659 \$INC PC-R6 :INCREMENT PC FOR PASS 1 ALSO
 03FF 660 BBCS #FLGSV_EVALEXPR,(R11),30\$;LIST NEW PC
 0403 661 30\$: RSB ;ALLOW EXPRESSION EVALUATION ON PASS 2 AGA
 0404 662 :
 0404 663 .DSABL LSB

0404 665 .SBTTL LABEL DEFINITIONS
 0404 666
 0404 667
 0404 668
 0404 669 ++
 0404 670 FUNCTIONAL DESCRIPTION:
 0404 671
 0404 672 THESE ROUTINES DEFINE LABELS. IF ENTRY IS AT LBL2 THE LABEL
 0404 673 IS DEFINED GLOBALLY. IF ENTRY IS AT LBL1 THE LABEL IS DEFINED
 0404 674 AS A LOCAL LABEL.
 0404 675
 0404 676 INPUTS:
 0404 677
 0404 678 MAC\$AL_VALSTACK-8[R7] (LBL2) SYMBOL BLOCK ADDRESS OF ID
 0404 679 MAC\$AL_VALSTACK-4[R7] (LBL1) SYMBOL BLOCK ADDRESS OF ID
 0404 680
 0404 681 OUTPUTS:
 0404 682
 0404 683 MAC\$GL LSB
 0404 684 INCREMENTED IF NOT LOCAL LABEL
 0404 685 AND 'ENABL LSB'
 0404 686
 0404 687
 0404 688 .ENABL LSB
 0404 689
 56 FFF8'CF47 D0 0404 690 LBL2:: :LABEL = ID DCOLON DCOLON
 09 A6 04 A8 0404 691 MOVL W^MAC\$AL_VALSTACK-8[R7],R6 ;POINT TO ID SYMBOL BLOCK
 09 A6 0800 8F A8 0404 692 BISW2 #SYMSM_G[OBL,SYMSW_FLAG(R6) ;MARK AS GLOBAL SYMBOL
 06 11 0414 0404 693 BISW2 #SYMSM_RELPSCT,SYMSW_FLAG(R6) ;ALWAYS OUTPUT GLOBAL SYMBOL
 0416 0404 694 BRB 10\$
 56 FFFC'CF47 D0 0416 696 LBL1:: :LABEL = ID DCOLON
 0416 0416 697 MOVL W^MAC\$AL_VALSTACK-4[R7],R6 ;POINT TO ID SYMBOL BLOCK
 041C 698 10\$:
 041C 699 LBL_X:: BBS #SYMSV_LOCAL,SYMSW_FLAG(R6),30\$;BRANCH IF LOCAL SYMBOL
 12 09 A6 06 E0 041C 700 BLBS W^ENBSG_LOCALSYMB+SYMSL_VAL,20\$;BRANCH IF ENABLE LSB
 03 0005'CF E8 0421 701 BSBW MAC\$SET_NEW LSB ;NO--MAKE A NEW LSB
 FBD7' 30 0426 702 BLBC W^ENBSG_DEBUG+SYMSL_VAL,30\$;BRANCH IF NO ENABLE DEBUG
 05 0005'CF E9 0429 703 20\$: BBCS #SYMSV_DEBUG,SYMSW_FLAG(R6),30\$;NO--TELL DEBUGGER ABOUT SYMBOL
 00 09 A6 05 E3 042E 704 BBC #SYMSV_DEF,SYMSW_FLAG(R6),40\$;SYMBOL ALREADY DEFINED?
 08 09 A6 00 E1 0433 705 30\$: SMAC_ERR_MULDEFLBL ; Yes--send message
 0438 706 BSBW MAC\$ERRORPT ;
 08 09 A6 03 E1 0440 707 40\$: BBC #SYMSV_EXTRN,SYMSW_FLAG(R6),50\$;IS SYMBOL EXTERNAL?
 0445 708 40\$: SMAC_ERR_SYMDCEXTR ; Yes--send message
 709 BSBW MAC\$ERRORPT ;
 05 A6 0000'CF D0 044D 710 50\$: MOVL W^MAC\$GL_PC,SYMSL_VAL(R6) ;SET SYMBOL VALUE
 0C A6 0000'CF 90 0453 711 50\$: MOVB W^MAC\$GL_PSECT,SYMSB_SEG(R6) ;SET PSECT NUMBER OF SYMBOL
 09 A6 01 A8 0459 712 BISW2 #SYMSM_DEF,SYMSW_FLAG(R6) ;MARK AS DEFINED
 55 0000'CF D0 045D 713 MOVL W^MAC\$GL_PSECTPTR,R5 ;POINT TO CURRENT PSECT
 04 0D A5 03 E0 0462 714 BBS #PSCSV_REL,PSCSW_OPTIONS(R5),60\$;BRANCH IF RELOCATABLE
 09 A6 10 A8 0467 715 BISW2 #SYMSM_ABS,SYMSW_FLAG(R6) ;NO--FLAG SYMBOL AS ABSOLUTE
 09 A5 0080 8F A8 046B 716 BISW2 #SYMSM_REF,PSCSW_FLAG(R5) ;MARK PSECT AS REFERENCED
 0471 717 60\$: SINTOUT_LW INT\$ LGLAB,R6 ;OUTPUT COMMAND TO PASS 2
 55 00'8F 9A 0479 718 MOVZBL #CRFSK_DEF,R5 ;SET DEFINITION
 FB80' 31 047D 720 BRW MAC\$CREF_SYM ;CREF SYMBOL IF CROSS REFERENCING
 0480 721

MAC\$ACTSTA
V04-000

MACHINE STATEMENTS
LABEL DEFINITIONS

K 8

0480 722

.DSABL LSB

16-SEP-1984 02:01:19 VAX/VMS Macro V04-00
5-SEP-1984 01:47:15 [MACRO.SRC]ACTSTA.MAR;1

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0480 724 .SBTTL DATA GENERATION DIRECTIVES

0480 725

0480 726 :++

0480 727 : FUNCTIONAL DESCRIPTION:

0480 728 :
0480 729 : BYTE/WORD/LONG/QUAD/SGNBYT/SGNWRD/OCTA ARE CALLED WHEN THE CORRESPONDING
0480 730 : DATA GENERATION DIRECTIVE IS SCANNED. FLAGS ARE SET FOR THE
0480 731 : ROUTINES DALST2, DALST1, AND DATNUL TO PROCESS THE FOLLOWING
0480 732 : DATA ITEMS.

0480 733 :
0480 734 :--

0480 735 :
0480 736 : BYTE::

00 0480 737 PUSHL #0
1F 10 0482 738 BSBB DAT_COM
01 0484 739 .BYTE 1

0480 740

0485 741 : WORD::

01 0485 742 PUSHL #1
1A 10 0487 743 BSBB DAT_COM
02 0489 744 .BYTE 2

048A 745

048A 746 : LONG::

02 048A 747 PUSHL #2
15 10 048C 748 BSBB DAT_COM
04 048E 749 .BYTE 4

048F 750

048F 751 : QUAD::

03 048F 752 PUSHL #3
10 10 0491 753 BSBB DAT_COM
08 0493 754 .BYTE 8

0494 755

0494 756 : SGNBYT::

04 0494 757 PUSHL #4
08 10 0496 758 BSBB DAT_COM
01 0498 759 .BYTE 1

0499 760

0499 761 : SGNWRD::

05 0499 762 PUSHL #5
06 10 0498 763 BSBB DAT_COM
02 049D 764 .BYTE 2

049E 765

049E 766 : OCTA::

06 049E 767 PUSHL #6
01 10 04A0 768 BSBB DAT_COM
10 04A2 769 .BYTE 16

04A3 770

04A3 771 : DAT_COM:

0000'CF 9E 9A 04A3 772 MOVZBL @(SP)+,W\$MACSGL_OPSIZE : STORE OPERAND SIZE
0000'CF 8ED0 04A8 773 POPL W\$MACSGL_DIRFLG : STORE INDEX
00 6B 06 E3 04AD 774 BBCS #FLGSV_EVALEXPR,(R11),.+1 ;ALLOW EXPRESSION EVALUATION

04B1 775 :
04B1 776 : CONTINUE ON INTO DATA_EXIT
04B1 777 :

04B1 779 :++
 04B1 780 : FUNCTIONAL DESCRIPTION:
 04B1 781 :
 04B1 782 : 'ADDRES' IS CALLED WHEN A .ADDRESS DIRECTIVE IS SCANNED.
 04B1 783 : ALL THAT IS DONE IS TO SET FLAGS AND ENSURE THAT THERE
 04B1 784 : IS ROOM IN THE INTERMEDIATE BUFFER TO CONTAIN THE EXPRESSION.
 04B1 785 :
 04B1 786 :--
 04B1 787 :
 04B1 788 : ADDRESS:
 04B1 789 : DATA_EXIT: ;ADDR_TYPE = KADDRESS
 0000'CF 59 D1 04B1 790 CMPL R9 W^MACSGL_INTHRNPT ;NEAR THE END OF THE BUFFER?
 03 03 1B 04B6 791 BLEQU 10\$;IF LEQ NO
 FB45' 30 04B8 792 BSBW MACSOUTFRAME ;YES--WRITE BUFFER OUT
 0000'CF 59 00 04B8 793 10\$: MOVL R9,W^MACSGL_EXPTR ;SAVE START OF EXPRESSION
 0000'CF 59 00 04C0 794 MOVL R9,W^MACSGL_EXPEND ;AND END OF EXPRESSION
 0000'CF D4 04C5 795 CLRL W^MACSGL_ABSFLAG ;ASSUME ABSOLUTE EXPR
 0000'CF D4 04C9 796 CLRL W^MACSGL_PRMSEG ;ABSOLUTE SEGMENT
 00 6B 04 E5 04CD 797 BBCC #FLGSV_DATRPT,(R11),.+1 ;NO REPEAT YET
 6B 00000084 8F C8 04D1 798 BISL2 #FLGSM_EXPORT!FLGSM_COMPEXPR,(R11) ;ALLOW EXPRESSION OPT.
 04D8 05 C 800 RSB ; AND ASSUME COMPILE TIME EXPR

04D9 802 :++
 04D9 803 : FUNCTIONAL DESCRIPTION:
 04D9 804 :
 04D9 805 : 'STOADR' IS CALLED FOR EACH ITEM FOUND IN A .ADDRESS DIRECTIVE.
 04D9 806 : CODE IS PUT IN THE INTERMEDIATE BUFFER TO STACK THE VALUE.
 04D9 807 : AND STORE POSITION INDEPENDENT DATA. FLAGS ARE THEN INITIALIZED
 04D9 808 : FOR THE NEXT ITEM.
 04D9 809 :
 04D9 810 :--
 04D9 811 :
 04D9 812 : STOADR:::
 0000'CF D5 04D9 813 TSTL W^MACSGL_ABSFLAG :ADDR_LIST = EXPR ! ADDR_LIST DCOMMA EXPR
 0E 12 04DD 814 BNEQ 10\$:ABSOLUTE EXPRESSION?
 FB1E' 30 04DF 815 BSBW MACSOPTIMIZEEXPR :IF NEQ NO
 04E2 816 SINTOUT_LW INTS STKL,<W^MACSAL_VALSTACK[R7]> :YES--WIPE IT OUT
 04ED 817 10\$: SINTOUT_X INTS_SPID :AND STACK THE VALUE
 04F3 818 SINC_PC #4 :STORE PIC DATA
 FFB6 31 04F8 819 BRW DATA_EXIT :COUNT FOUR BYTES
 04FB 820 :INIT FOR NEXT ADDRESS
 04FB 821 :++
 04FB 822 : FUNCTIONAL DESCRIPTION:
 04FB 823 :
 04FB 824 : 'DATARG' IS CALLED FOR EACH ITEM IN A BYTE/WORD/LONG/QUAD
 04FB 825 : DIRECTIVE. FLAGS ARE INITIALIZED FOR THE NEXT ITEM.
 04FB 826 :
 04FB 827 :--
 04FB 828 :
 04FB 829 : DATARG:::
 0000'CF D5 04FB 830 TSTL W^MACSGL_ABSFLAG :DATA_LIST = EXPR
 04 13 04FF 831 BEQL 10\$:DATA_LIST = DATA_LIST DCOMMA EXPR
 00 6B 07 E5 0501 832 BBCC #FLGSV_EXPOPT,(R11),10\$:ABSOLUTE EXPRESSION?
 0505 833 :IF EQL YES
 0505 834 10\$: #NO--NO OPTIMIZATION
 0505 835 :
 0505 836 : THE FOLLOWING ALLOWS EVALUATION OF REPEAT COUNT
 0505 837 :
 00 6B 02 E3 0505 838 BBCS #FLGSV_COMPEXPR,(R11),..+1 ;ASSUME COMPILE TIME EXPRESSION
 0000'CF D4 0509 839 CLRL W^MACSGL_ABSFLAG ;ASSUME ABSOLUTE
 0000'CF D4 0500 840 CLRL W^MACSGL_PRMSEG ;ABS PSECT
 00 6B 04 E5 0511 841 BBCC #FLGSV_DATRPT,(R11),..+1 ;NO REPEAT COUNT YET
 05 0515 842 RSB
 0516 843 :
 0516 844 :++
 0516 845 : FUNCTIONAL DESCRIPTION:
 0516 846 :
 0516 847 : 'DATNUL' IS CALLED WHEN A NULL DATA ITEM IS FOUND IN A
 0516 848 : BYTE/WORD/LONG/QUAD/OCTA DIRECTIVE. A ZERO VALUE IS EMITTED
 0516 849 : TO PASS 2 AND FLAGS ARE INITIALIZED FOR THE NEXT ITEM.
 0516 850 :
 0516 851 :--
 0516 852 :
 0516 853 : DATNUL:::
 50 55 0000'CF D0 0516 854 MOVL W^MACSGL_DIRFLG,R5 :DATA_STAT = DATA_TYPE <NULL>
 00000000'E5 9A 0518 855 MOVZBL L^DAT_NUL_CMD(R5),R0 :GET INDEX FOR DATA TYPE
 00 DD 0522 856 PUSHL #0 :GET COMMAND
 FAD9' 30 0524 857 BSBW MACSINTOUT 1 LW :STACK A 0
 53 00000031'E5 9A 0527 858 MOVZBL L^DAT_SHIFT_FACT(R5),R3 :SEND TO INT. BUFFER
 :Get shift factor

03	53	91	052E	859	CMPB	R3 #3	: Was this .QUAD or .OCTA?
	1D	19	0531	860	BLSS	10\$: No if LSS
			0533	861	\$INTOUT	LW INT\$_STIL,<#0>	: Set bits 32-63 as zero
04	53	91	053B	862	CMPB	R3 #4	: Was this .OCTA?
	10	12	053E	863	BNEQ	10\$: No if NEQ
			0540	864	\$INTOUT	LW INT\$_STIL,<#0>	: Set bits 64-95 and
			0548	865	\$INTOUT	LW INT\$_STIL,<#0>	: bits 96-127 as zero
			0550	866	\$INC_PC	W^MACSGE OPSIZE	: COUNT THE BYTES
	FF57	31	0557	867	BRW	DATA_EXIT	: INIT FOR NEXT ITEM
				10\$:			

055A 869 :++
 055A 870 : FUNCTIONAL DESCRIPTION:
 055A 871 :
 055A 872 : 'DALST2' AND 'DALST1' ARE CALLED TO PROCESS THE ITEMS IN
 055A 873 : A DATA-LIST FOR BYTE/WORD/LONG/QUAD/OCTA DIRECTIVES. 'DALST2'
 055A 874 : IS CALLED IF THIS IS A REPEAT ITEM, AND 'DALST1' IS CALLED
 055A 875 : IF IT IS NOT.
 055A 876 :
 055A 877 :--
 055A 878 :
 6B 04 E3 055A 879 DALST2:: :DATA_ARGS = DATA_LIST DSQOPN EXPR DSQCLS
 03 055A 880 BBCS #FLGSV_DATRPT,(R11),- ;THIS IS REPEATED DATA
 055D 881 DALST1:
 FF9A 30 055E 882 QUDSTR:: :DATA_STAT = QUAD_HEAD PRIMITIVE
 055E 883 OCTSTR:: :DATA_STAT = OCTA_HEAD PRIMITIVE
 55 0000'CF 2D 6B 04 0561 884 BSBW DATARG :INIT DATA FLAGS
 D0 0561 885 DALST1:: :DATA_ARGS = DATA_LIST
 E1 0566 886 MOVL U^MACSGL_DIRFLG,R5 :GET DATA TYPE INDEX
 056A 887 BBC #FLGSV_DATRPT,(R11),30\$;BRANCH IF NOT REPEAT
 056A 888 :
 056A 889 : THIS IS REPEATED DATA TYPE
 056A 890 :
 0000'CF 08 056A 891 TSTL U^MACSGL_ABSFLAG :IS REPEAT COUNT ABSOLUTE?
 50 FFFC'CF47 0D 056E 892 BNEQ 10\$:IF NEQ NO--ERROR
 0570 893 MOVL U^MACSAL_VALSTACK-4[R7],R0 ;YES--GET REPEAT COUNT
 0576 894 BRB 20\$:AND SKIP AHEAD
 FA80' 30 0578 895 10\$: \$MAC_ERR RPTCNTNABS :No--get error code
 FFFC'CF47 D4 0580 896 BSBW MACSERRORPT :ISSUE MESSAGE TO PASS 2
 50 00000007'E5 9A 0585 897 CLRL U^MACSAL_VALSTACK-4[R7] :DO NO REPEATING
 FA71' 30 058C 898 20\$: MOVZBL L^DAT RPT CMD(R5),R0 :GET COMMAND
 50 FFFC'CF47 36 058F 899 BSBW MACSINTOUT_X :ISSUE TO PASS 2
 11 0595 900 MOVL U^MACSAL_VALSTACK-4[R7],R0 ;GET THE REPEAT COUNT
 BRB 60\$:FINISH UP
 0597 902 :
 0597 903 : NOT A REPEAT
 0597 904 :
 25 6B 07 E1 0597 905 30\$: BBC #FLGSV_EXPOPT,(R11),40\$;BRANCH IF NOT OPTIMIZABLE
 FA62' 30 059B 906 BSBW MACSOPTIMIZEEXPR :YES--WIPE OUT EXPRESSION
 0000'CF47 DD 059E 907 PUSHL U^MACSAL_VALSTACK[R7] :STACK THE VALUE
 50 00000015'EF45 02 05A3 908 MOVL L^DAT_TRUNC_CHK[R5],R0 :GET TRUNCATION ROUTINE CHECK ADDRESS
 05A3 909 BEQL 33\$:IF EQL NO NEED TO CHECK
 60 16 05AB 910 JSB (R0) :CHECK FOR TRUNCATION AND REPORT ERROR
 55 0000'CF 60 05AD 911 33\$: MOVL U^MACSGL_DIRFLG,R5 :RETRIEVE DATA TYPE INDEX AGAIN
 00000000'E5 9A 05AF 912 MOVZBL L^DAT_NUC_CMD(R5),R0 :GET THE COMMAND
 FA42' 30 05B4 913 35\$: BSBW MACSINTOUT_1_LW :SEND TO INT. FILE
 0A 11 05BE 914 BRB 50\$:CONTINUE
 05C0 915 :
 05C0 916 : NOT OPTIMIZED, NOT REPEATED
 05C0 917 :
 50 0000000E'E5 9A 05C0 918 40\$: MOVZBL L^DAT_STO_CMD(R5),R0 :GET COMMAND
 FA36' 30 05C7 919 BSBW MACSINTOUT_X :SEND TO INT. FILE
 50 01 9A 05CA 920 50\$: MOVZBL #1,R0 :USE REPEAT COUNT OF 1
 05CD 921 :
 05CD 922 : FINISH UP
 05CD 923 :
 53 00000031'E5 9A 05CD 924 60\$: MOVZBL L^DAT_SHIFT_FACT(R5),R3 :Get shift factor
 50 50 53 78 05D4 925 ASHL R3,R0,R0 :Figure total allocation

03 53 91 05D8 926 \$INC_PC R0 :COUNT IN PASS 1
24 19 05DD 927 CMPB R3 #3 : Was this .QUAD or .OCTA
BLSS 70\$ #3 : No if LSS
04 53 91 05E2 928 \$INTOUT_LW INT\$_STIL,<W^MAC\$GL_VAL3> ; Send bits 32-63 to intermediate file
14 12 05EC 930 CMPB R3 #4 : Was this .OCTA?
BNEQ 65\$ #4 : No if NEQ
05F1 932 \$INTOUT_LW INT\$_STIL,<W^MAC\$GQ_VAL2+0> ; Send bits 64-95 and then
05FB 933 \$INTOUT_LW INT\$_STIL,<W^MAC\$GQ_VAL2+4> ; bits 96-127 to intermediate file
0605 934 65\$: RSB
FEAB 31 0606 936 70\$: BRW DATA_EXIT :INIT FOR NEXT ELEMENT

0609 938 .SBTTL ENTRY POINT DEFINITION DIRECTIVES
 0609 939
 0609 940 ++
 0609 941 : FUNCTIONAL DESCRIPTION:
 0609 942 : VECTRO IS CALLED WHEN A .VECTOR DIRECTIVE WITH NO EPT MASK
 0609 943 : IS SCANNED.
 0609 944 :--
 0609 945 :
 0609 946 :
 0609 947 :
 0609 948 : VECTRO:: : DIRECTIVE = KVECTOR ID
 0609 949 : \$INTOUT_LW INT\$ STKEPT,<W^MACSAL_VALSTACK[R7]> ;STACK ENTRY POINT MASK
 69 11 0614 950 : \$INTOUT_X INT\$ STOW : Store word
 061A 951 : BRB ENTRY_VEC_XIT : TAKE COMMON EXIT
 061C 952 :
 061C 953 :++
 061C 954 : FUNCTIONAL DESCRIPTION:
 061C 955 :
 061C 956 : VECTR2 AND VECTR1 ARE CALLED WHEN .VECTOR DIRECTIVES ARE
 061C 957 : SCANNED WITH AN EPT MASK. CODE IS EMITTED TO STACK THE
 061C 958 : EPT AND OR IT WITH THE EXPRESSION ON THE STACK.
 061C 959 :
 061C 960 :--
 061C 961 :
 061C 962 : VECTR2:: : DIRECTIVE = KVECTOR ID EXPR
 52 FFFC'CF47 00 061C 963 : MOVL W^MACSAL_VALSTACK-4[R7],R2 ;POINT TO SYMBOL
 06 11 0622 964 : BRB VEC_COM :
 0624 965 :
 52 FFF8'CF47 00 0624 966 : VECTR1:: : DIRECTIVE = KVECTOR ID DCOMMA EXPR
 0624 967 : MOVL W^MACSAL_VALSTACK-8[R7],R2 ;POINT TO SYMBOL
 062A 968 : VEC_COM:
 062A 969 : \$INTOUT_LW INT\$ STKEPT,R2 :STACK EPT
 0632 970 : \$INTOUT_X INT\$ OR :OR WITH EXPR ON STACK
 0638 971 : \$INTOUT_X INT\$ STOW : Store word
 24 11 063E 972 : BRB ENTRY_VECTOR :
 0640 973 :
 0640 974 :++
 0640 975 : FUNCTIONAL DESCRIPTION:
 0640 976 :
 0640 977 : ENTRY1 AND ENTRY2 ARE CALLED TO PROCESS .ENTRY DIRECTIVES. THE
 0640 978 : ONLY DIFFERENCE BETWEEN THEM IS THAT ENTRY1 IS CALLED IF THERE
 0640 979 : WAS A COMMA BETWEEN THE ID AND THE EXPRESSION AND ENTRY2 IS
 0640 980 : CALLED IF THERE WAS NO COMMA.
 0640 981 :
 0640 982 :--
 0640 983 :
 56 FFF8'CF47 00 0640 984 : ENTRY1:: : DIRECTIVE = KENTRY ID DCOMMA EXPR
 06 11 0640 985 : MOVL W^MACSAL_VALSTACK-8[R7],R6 ;POINT TO SYMBOL BLOCK
 0646 986 : BRB ENTRY_COM :
 0648 987 :
 56 FFFC'CF47 00 0648 988 : ENTRY2:: : DIRECTIVE = KENTRY ID EXPR
 0648 989 : MOVL W^MACSAL_VALSTACK-4[R7],R6 ;POINT TO SYMBOL BLOCK
 064E 990 : ENTRY_COM:
 064E 991 : BISW2 #SYMSM_EPT!SYMSM_GLOBL,- ;MARK AS GLOBAL EPT
 0204 8F A8 064E 992 : SYMSW_FLAG(R6) :
 09 A6 0652 993 :
 FDC5 30 0654 993 : BSBW LBL_X : DEFINE LABEL
 0657 994 : \$INTOUT_LW INT\$ EPT,<R6,W^MACSAL_VALSTACK[R7]> ;PROCESS EPT ON PASS 2

0000'CF 00003003 0664 995 ENTRY_VECTOR:
 13 D5 0664 996 TSTL W^MACSGL_ABSFLAG :ABSOLUTE EXPR?
 12 0668 997 BNEQ 10S :IF NEQ NO
 0000'CF47 00003003 066A 998 BITL #^X3003,W^MACSAL_VALSTACK[R7] :YES--ANY ILLEGAL BITS SET?
 OF 13 0674 999 BEQL 20S :IF EQL NO
 05 11 0676 1000 \$MAC_ERR_ILLMASKBIT :Yes--get message code
 067D 1002 10S: \$MAC_ERR_EM_SKNOTABS
 F97B' 30 0682 1003 15S: BRB 15S
 0685 1004 20S: BSBW MACSERROPR :Entry mask not absolute
 0685 1005 ENTRY_VEC_XIT: :REPORT TO PASS 2
 0685 1006 \$INC_PC #2 :COUNT TWO BYTES
 50 00000000'EF D0 068A 1007 MOVL MACSGL_PSECTPTR, R0 :AND MARK THE PSECT AS REFERENCED.
 09 A0 0080 8F A8 0691 1008 BISW2 #SYMSM_REF,PSCSW_FLAG(R0)
 05 0697 1009 RSB
 0698 1010
 0698 1011 ++
 0698 1012 : FUNCTIONAL DESCRIPTION:
 0698 1013 :
 0698 1014 : THIS ROUTINE IS CALLED TO PROCESS THE .TRANSFER DIRECTIVE.
 0698 1015 : CODE IS EMITTED TO PASS 2 TO SEND A REDEFINITION COMMAND
 0698 1016 : TO THE LINKER.
 0698 1017 :
 0698 1018 :--
 0698 1019 :
 0698 1020 XFER:: :DIRECTIVE = KXFER ID
 0698 1021 \$INTOUT_LW INT\$ REDEF,<W^MACSAL_VALSTACK[R7]> :TELL PASS 2
 50 00000000'EF D0 06A3 1022 MOVL MACSGL_PSECTPTR, R0 :AND MARK THE PSECT AS REFER
 01 0C A0 91 06AA 1023 CMPB PSCSB_SEG(R0), #1 :ARE WE DEALING WITH
 06 12 06AE 1024 BNEQ 10S :THE BLANK PSECT?
 09 A0 0080 8F A8 06B0 1025 BISW2 #SYMSM_REF,PSCSW_FLAG(R0) :YES MARK IT AS REFERENCED.
 05 06B6 1026 10S: RSB
 06B7 1027
 06B7 1028 .END

SCOUNT	= 0000003B		CHR\$V_SPA_MSK	= 00000000		FLG\$M_MOREARG	= 00002000
AB	= 00000001		CHR\$V_SYM_CH1	= 00000003		FLG\$M_MOREINP	= 00000008
AD	= 00000008		CHR\$V_SYM_CHR	= 00000002		FLG\$M_NEWPND	= 00000400
ADDRES	= 000004B1	RG 04	CHR\$V_SYM_DLM	= 00000001		FLG\$M_NOREF	= 01000000
ADMS_ABSOLUTE	= 00000002		CNT	= 00000001		FLG\$M_NTYPEDPC	= 00000020
ADMS_BYTE_DISP	= 0000000A		CR	= 0000000D	X 04	FLG\$M_NULCHR	= 00040000
ADMS_DFBYTEDISP	= 0000000B		CRFSK_DEF	*****		FLG\$M_OBJXST	= 00200000
ADMS_DFLONGDISP	= 0000000F		D	= 0000C008		FLG\$M_OPNDCHK	= 00000100
ADMS_DFRAUTODINC	= 00000009		DALST1	= 00000561	RG 04	FLG\$M_OPRND	= 00002000
ADMS_DFWORDDISP	= 0000000D		DALST2	= 0000055A	RG 04	FLG\$M_OPTVFLIDX	= 00001000
ADMS_IMMEDIATE	= 00000001		DATARG	= 000004FB	RG 04	FLG\$M_ORDLST	= 00020000
ADMS_INDEX	= 00000004		DATA_EXIT	= 000004B1	R 04	FLG\$M_P2	= 00004000
ADMS_LITERAL	= 00000000		DATNUL	= 00000516	RG 04	FLG\$M_RPTIRP	= 10000000
ADMS_LONG_DISP	= 0000000E		DAT_COM	= 000004A3	R 04	FLG\$M_SEQFIL	= 02000000
ADMS_MAXMOD	= 0000000F		DAT_NUL_CMD	= 00000000	R 03	FLG\$M_SKAN	= 00008000
ADMS_PIC	= 0C000003		DAT_RPT_CMD	= 00000007	R 03	FLG\$M_SPECOP	= 00000004
ADMS_REGAUTODEC	= 00000007		DAT_SHIFT_FACT	= 00000031	R 03	FLG\$M_SPLALL	= 04000000
ADMS_REGAUTODINC	= 00000008		DAT_STO_CMD	= 0000000E	R 03	FLG\$M_STOIMF	= 00040000
ADMS_REGISTER	= 00000005		DAT_TRUNC_CHK	= 00000015	R 03	FLG\$M_SYM2COL	= 00000400
ADMS_RRIND	= 00000006		ENBSG_DEBUG	*****	X 04	FLG\$M_TOCFLG	= 00080000
ADMS_WORD_DISP	= 0000000C		ENBSG_LOCALSYMB	*****	X 04	FLG\$M_UPAFLG	= 00000010
AF	= 00008004		ENBSG_SUPPRESS	*****	X 04	FLG\$M_UPDFIL	= 00000080
AG	= 0000A008		ENTRYT	= 00000640	RG 04	FLG\$M_UPMARG	= 00000040
AH	= 00009010		ENTRY2	= 00000648	RG 04	FLG\$V_XCRF	= 80000000
AL	= 00000004		ENTRY_COM	= 0000064E	R 04	FLG\$V_ALLCHR	= 00000000
AO	= 00000010		ENTRY_VECTOR	= 00000664	R 04	FLG\$V_BOL	= 00000001
AO	= 00000008		ENTRY_VEC_XIT	= 00000685	R 04	FLG\$V_CHKLPND	= 00000014
ARG\$K_SIZE	= 00003E8		ERR	= 00000000		FLG\$V_COMPEXPR	= 00000002
ASSGNT	= 00000285	RG 04	F	= 00008004		FLG\$V_CONT	= 00000003
ASSHD1	= 0000023C	RG 04	FF	= 0000000C		FLG\$V_CRF	= 0000001E
ASSHD2	= 0000022A	RG 04	FLG\$M_ALLCHR	= 00000001		FLG\$V_CRSEEN	= 00000020
ASSHD3	= 00000224	RG 04	FLG\$M_BOL	= 00000002		FLG\$V_DATRPT	= 00000004
ASSIGN_HEAD	= 00000242	R 04	FLG\$M_CHKLPND	= 00100000		FLG\$V_DBGOUT	= 0000002E
AUD\$K_SIZE	= 00000010		FLG\$M_COMPEXPR	= 00000004		FLG\$V_DLIMSTR	= 0000002F
AW	= 00000002		FLG\$M_CONT	= 00000008		FLG\$V_ENDMCH	= 00000005
B	= 00000001		FLG\$M_CRF	= 40000000		FLG\$V_EVALEXPR	= 00000006
BLKBYT	= 0000039A	RG 04	FLG\$M_CRSEEN	= 00000001		FLG\$V_EXPORT	= 00000007
BLKLNG	= 000003A0	RG 04	FLG\$M_DATRPT	= 00000010		FLG\$V_EXTERR	= 00000030
BLKOCT	= 000003A6	RG 04	FLG\$M_DBGOUT	= 00004000		FLG\$V_EXTWRN	= 00000031
BLKOUD	= 000003A3	RG 04	FLG\$M_DLIMSTR	= 00008000		FLG\$V_FIRSTLNL	= 00000029
BLKWRD	= 0000039D	RG 04	FLG\$M_ENDMCH	= 00000020		FLG\$V_IFSTAT	= 00000017
BLNK	= 00000020		FLG\$M_EVALEXPR	= 00000040		FLG\$V_IIF	= 00000016
BSTAT1	= 0000038A	RG 04	FLG\$M_EXPORT	= 00000080		FLG\$V_INSERT	= 00000008
BSTAT2	= 000003C5	RG 04	FLG\$M_EXTERR	= 00010000		FLG\$V_IRPC	= 00000010
BYTE	= 00000480	RG 04	FLG\$M_EXTWRN	= 00020000		FLG\$V_LEXOP	= 00000021
CHR\$M_COMMACR	= 00000020		FLG\$M_FIRSTLNL	= 00000200		FLG\$V_LSTXST	= 00000009
CHR\$M_ILL_CRR	= 00000040		FLG\$M_IFSTAT	= 00800000		FLG\$V_MAC2COL	= 00000028
CHR\$M_NUM_BER	= 00000010		FLG\$M_IIF	= 00400000		FLG\$V_MACL	= 00000008
CHR\$M_SPA_MSK	= 00000001		FLG\$M_INSERT	= 00000100		FLG\$V_MACLTB	= 0000001B
CHR\$M_SYM_CH1	= 00000008		FLG\$M_IRPC	= 20000000		FLG\$V_MACTXT	= 00000010
CHR\$M_SYM_CHR	= 00000004		FLG\$M_LEXOP	= 00000002		FLG\$V_MEBLST	= 0000000C
CHR\$M_SYM_DLM	= 00000002		FLG\$M_LSTXST	= 00000200		FLG\$M_MOREARG	= 00000020
CHR\$V_COMMACR	= 00000005		FLG\$M_MAC2COL	= 00000800		FLG\$M_MOREINP	= 00000023
CHR\$V_CVTLWC	= 00000061		FLG\$M_MACL	= 00000800		FLG\$M_NEWPND	= 0000000A
CHR\$V_ILL_CHR	= 00000006		FLG\$M_MACLTB	= 08000000		FLG\$M_NOREF	= 00000018
CHR\$V_NOCVT	= 0000007F		FLG\$M_MACTXT	= 00010000		FLG\$M_NTYPEDPC	= 00000025
CHR\$V_NUM_BER	= 00000004		FLG\$M_MEBLST	= 00001000		FLG\$M_NULCHR	= 00000032

FLGSV_OBJXST	= 00000015	INT\$-SBTTL	= 00000021	MAC\$GL_HIGH_32	*****	X	04	
FLGSV_OPNDCHK	= 00000028	INT\$-SETFLAG	= 00000022	MAC\$GL_INTWRNPT	*****	X	04	
FLGSV_OPRND	= 0000000D	INT\$-SETLONG	= 00000023	MAC\$GL_LINBAS	*****	X	04	
FLGSV_OPTVFLIDX	= 0000002C	INT\$-SPIC	= 00000024	MAC\$GL_LINENUM	*****	X	04	
FLGSV_ORDLST	= 00000011	INT\$-SPID	= 00000025	MAC\$GL_MOPNUM	*****	X	04	
FLGSV_P2	= 0000000E	INT\$-STIB	= 00000026	MAC\$GL_MOPPTR	*****	X	04	
FLGSV_RPTIRP	= 0000001C	INT\$-STIL	= 00000028	MAC\$GL_OPSIZE	*****	X	04	
FLGSV_SEQFIL	= 00000019	INT\$-STIW	= 00000027	MAC\$GL_PC	*****	X	04	
FLGSV_SKAN	= 0000000F	INT\$-STKEPT	= 00000029	MAC\$GL_PRMSEG	*****	X	04	
FLGSV_SPECOP	= 00000022	INT\$-STKG	= 0000002A	MAC\$GL_PSECT	*****	X	04	
FLGSV_SPLALL	= 0000001A	INT\$-STKL	= 0000002B	MAC\$GL_PSECTPTR	*****	X	04	
FLGSV_STOIMF	= 00000012	INT\$-STKPC	= 0000002C	MAC\$GL_RECHDBUF	*****	X	04	
FLGSV_SYM2COL	= 0000002A	INT\$-STKS	= 0000002D	MAC\$GL_SAVE_PC	*****	X	04	
FLGSV_TOCFLG	= 00000013	INT\$-STOB	= 00000034	MAC\$GL_SAV_BAS	*****	X	04	
FLGSV_UPAFLG	= 00000024	INT\$-STOL	= 0000002E	MAC\$GL_SAV_LIN	*****	X	04	
FLGSV_UPDFIL	= 00000027	INT\$-STOW	= 00000035	MAC\$GL_SAV_PAG	*****	X	04	
FLGSV_UPMARG	= 00000026	INT\$-STRB	= 0000002F	MAC\$GL_SRCPAG	*****	X	04	
FLGSV_XCRF	= 0000001F	INT\$-STRL	= 00000031	MAC\$GL_VAL3	*****	X	04	
G	= 0000A008	INT\$-STRSB	= 00000032	MAC\$GL_VALUE	*****	X	04	
H	= 00009010	INT\$-STRSW	= 00000033	MAC\$GQ_HIGH_64	*****	X	04	
HASHSZ	= 0000007F	INT\$-STRW	= 00000030	MAC\$GQ_VAL2	*****	X	04	
HYPHEN	= 0000002D	INT\$-STS	= 00000036	MAC\$INTOUT_1_LW	*****	X	04	
INPSK_BUFSIZ	= 00003E8	INT\$-STSW	= 00000037	MAC\$INTOUT_2_LW	*****	X	04	
INTSK_BUFSIZ	= 00013F4	INT\$-SUB	= 0000000A	MAC\$INTOUT ASN	0000035B RG		04	
INTSK_BUFWRN	= 0001390	INT\$-SUME	= 00000039	MAC\$INTOUT_N	*****	X	04	
INTS_ADD	= 00000001	INT\$-WRN	= 00000038	MAC\$INTOUT_WD	*****	X	04	
INTS_AND	= 00000002	INT\$-XOR	= 0000000B	MAC\$INTOUT_X	*****	X	04	
INTS_ASH	= 00000003	L	= 00000004	MAC\$MUL_DEF_CHK	0000037C RG		04	
INTS ASN	= 0000000C	LBL1	00000416 RG	04	MAC\$OPTIMIZEEXPR	*****	X	04
INTS AUGPC	= 0000000D	LBL2	00000404 RG	04	MAC\$OUTFRAME	*****	X	04
INTS_BDST	= 0000000E	LBL_X	0000041C R	04	MAC\$SET_NEW_LSB	*****	X	04
INTS_CHKL	= 0000000F	LONG	0000048A RG	04	MAC\$SET_PC	*****	X	04
INTS_DIV	= 00000004	LSTSK_BUFSIZ	= 00000086	MAC\$ASGNMNTSYN=	007D9022			
INTS_END	= 00000010	LSTSK_L_P PAGE	= 0000003C	MAC\$BLKEXPNAbs=	007D904A			
INTS_EPT	= 00000011	LSTSK_TITLE SIZ=	00000028	MAC\$EMSKNOTABS=	007D9072			
INTS_ERR	= 00000012	MAC\$AB_LINEBF	*****	MAC\$ILLBRDEST=	007D90BA			
INTS_ETX	= 00000013	MAC\$AL_VALSTACK	*****	MAC\$ILLMASKBIT=	007D90FA			
INTS_FNEWL	= 00000014	MAC\$AW_ILLMODTB	*****	MAC\$ILLMODE=	007D9102			
INTS_ILG	= 00000000	MAC\$CK_BYT_TRU1	*****	MAC\$MULDEFLBL=	007D915A			
INTS_INFO	= 0000003A	MAC\$CK_SBY_TRU1	*****	MAC\$NOTENUFOPR=	007D917A			
INTS_LGLAB	= 00000015	MAC\$CK_SWD_TRU1	*****	MAC\$OPRNDSYNX=	007D91A2			
INTS_MACL	= 00000016	MAC\$CK_WRD_TRU1	*****	MAC\$RPTCNTNABS=	007D91D2			
INTS_MUL	= 00000005	MAC\$CREF_OPCODE	*****	MAC\$SYMDCLEXTR=	007D91DA			
INTS_NEG	= 00000006	MAC\$CREF_SYM	*****	MAC\$TOOMNYOPND=	007D9202			
INTS_NEWL	= 00000017	MAC\$ERRORPT	*****	MAC\$OP EXIT	0000003A R	04		
INTS_NEWP	= 00000018	MAC\$ERRORPX	*****	MAC\$SUBSYS	= 0000007D			
INTS_NOT	= 00000007	MAC\$GB_MODE	*****	MB	= 00000041			
INTS_OP	= 00000019	MAC\$GB_RDXNDX	*****	MD	= 00000048			
INTS_OR	= 00000008	MAC\$GB_REG	*****	MF	= 00008044			
INTS_PRIL	= 0000001A	MAC\$GB_VAL3	*****	MG	= 0000A048			
INTS_PRT	= 0000001B	MAC\$GL_ABSFLAG	*****	MH	= 00009050			
INTS_PSECT	= 0000001C	MAC\$GL ASNPTR	*****	MINST1	0000000F RG	04		
INTS_REDEF	= 0000001D	MAC\$GL_DIRFLG	*****	ML	= 00000044			
INTS_REF	= 0000001E	MAC\$GL_ERRPTX	*****	MO	= 00000050			
INTS_REST	= 0000001F	MAC\$GL_EXPEND	*****	MQ	= 00000048			
INTS_SAME	= 00000009	MAC\$GL_EXPOVVL1	*****	MW	= 00000042			
INTS_SAVE	= 00000020	MAC\$GL_EXPPTR	*****	O	= 00000010			

OBJ\$K_BUFSIZ	= 00000200		PSCSM_QUAD	= 00004C00		SYMSL_VAL	= 00000005	
OCTA	= 0000049E	RG	PSCSM_RD	= 00000080		SYMSM_ABS	= 00000010	
OCTSTR	= 0000055E	RG	PSCSM_REL	= 00000008		SYMSM ASN	= 00000100	
OPDSM_ADDR	= 00000000		PSCSM_SHR	= 00000020		SYMSM_CRF0	= 00002000	
OPDSM_BB	= 000000A1		PSCSM_USR	= FFFFFFFD		SYMSM_DEBUG	= 00000020	
OPDSM_BW	= 000000C2		PSCSM_VEC	= 00000200		SYMSM_DEF	= 00000001	
OPDSM_D_FLOAT	= 0000C000		PSCSM_WORD	= 00004400		SYMSM_DELMAC	= 00000200	
OPDSM_FFLOAT	= 00008000		PSCSM_WRT	= 00000180		SYMSM_EPT	= 00000200	
OPDSM_G_FLOAT	= 0000A000		PSCSS_ALIGNMENT	= 00000004		SYMSM_EXTRN	= 00000008	
OPDSM_H_FLOAT	= 00009000		PSCSV_ALIGNMENT	= 0000000E		SYMSM_GLOBL	= 00000004	
OPDSM_MODE	= 000003E0		PSCSV_EXE	= 00000006		SYMSM_LOCAL	= 00000040	
OPDSM MODIFY	= 00000040		PSCSV_GBL	= 00000004		SYMSM_ODBG	= 00000400	
OPDSM_NOT 32F	= 00007000		PSCSV_LIB	= 00000001		SYMSM_REF	= 00000080	
OPDSM_READ	= 00000020		PSCSV_OVR	= 00000002		SYMSM_RELSECT	= 00000800	
OPDSM_VIELD	= 00000080		PSCSV_PIC	= 00000000		SYMSM_SUPR	= 00004000	
OPDSM_WRITE	= 00000060		PSCSV_RD	= 00000007		SYMSM_WEAK	= 00000002	
OPDS\$ MODE	= 00000005		PSCSV_REL	= 00000003		SYMSM_XCRF	= 00001000	
OPDS\$ SIZE	= 00000005		PSCSV_SHR	= 00000005		SYMSV_ABS	= 00000004	
OPDSV_D_FLOAT	= 0000000E		PSCSV_VEC	= 00000009		SYMSV ASN	= 00000008	
OPDSV_FFLOAT	= 0000000F		PSCSV_WRT	= 00000008		SYMSV_CRF0	= 0000000D	
OPDSV_G_FLOAT	= 0000000D		PSCSW_FLAG	= 00000009		SYMSV_DEBUG	= 00000005	
OPDSV_H_FLOAT	= 0000000C		PSCSW_OPTIONS	= 0000000D		SYMSV_DEF	= 00000000	
OPDSV_MODE	= 00000005		Q	= 00000008		SYMSV_DELMAC	= 00000009	
OPDSV_SIZE	= 00000000		QUAD	= 000048F	RG	04	SYMSV_EXTRN	= 00000003
OPFSM_LASTOPR	= 00002000		QUDSTR	= 000055E	RG	04	SYMSV_GLOBL	= 00000002
OPFSM_OPTEXP	= 00001000		RB	= 00000021		SYMSV_LOCAL	= 00000006	
OPFSV_LASTOPR	= 0000000D		RD	= 0000C028		SYMSV_ODBG	= 0000000A	
OPFSV_OPTEXP	= 0000000C		RDXSV_BINARY	= 00000000		SYMSV_REF	= 00000007	
OPRAND	0000008C	RG	RDXSV_DECIMAL	= 00000002		SYMSV_RELSECT	= 0000000B	
PSC\$B_NAME	00000004		RDXSV_DOUBLE	= 00000005		SYMSV_SUPR	= 0000000E	
PSC\$B_SEG	0000000C		RDXSV_FLOAT	= 00000004		SYMSV_WEAK	= 00000001	
PSC\$B_UNUSED	0000000B		RDXSV_GFLOAT	= 00000006		SYMSV_XCRF	= 0000000C	
PSC\$K_BLKSIZ	00000013		RDXSV_HEX	= 00000003		SYMSW_FLAG	= 00000009	
PSC\$K_NO_OPTNS	= 0000000A		RDXSV_HFLOAT	= 00000007		TAB	= 00000009	
PSC\$L_CURLOC	0000000F		RDXSV_OCTAL	= 00000001		VB	= 00000081	
PSC\$L_LINK	00000000		REGS_PC	= 0000000F		VD	= 0000C088	
PSC\$L_MAXLGTH	00000005		RF	= 00008024		VECTRO	00000609 RG 04	
PSCSM_ABS	= FFFFFFF7		RG	= 0000A028		VECTR1	00000624 RG 04	
PSCSM_ALIGNFLG	= 00004000		RH	= 00009030		VECTR2	0000061C RG 04	
PSCSM_ALLOPTNS	= 000003FF		RL	= 00000024		VEC_COM	0000062A R 04	
PSCSM_BYTE	= 00004000		RO	= 00000030		VF	= 00008084	
PSCSM_CON	= FFFFFFFB		RQ	= 00000028		VG	= 0000A088	
PSCSM_DEFAULT	= 000001C8		RW	= 00000022		VH	= 00009090	
PSCSM_EXE	= 000000C0		SEMI	= 00000038		VL	= 00000084	
PSCSM_GBL	= 00000010		SGNBYT	= 00000494	RG	04	VO	= 00000090
PSCSM_LCL	= FFFFFFFF		SGNWRD	= 00000499	RG	04	VQ	= 00000088
PSCSM_LIB	= 00000002		STAT1	= 00000000	RG	04	VW	= 00000082
PSCSM_LONG	= 00004800		STBSK_PG_MISS	= 0000000A		W	= 00000002	
PSCSM_NOEXE	= FFFFFFFBF		STOADR	= 000004D9	RG	04	WB	= 00000061
PSCSM_NOPIC	= FFFFFFFFE		SYMSB_NAME	= 00000004		WD	= 0000C068	
PSCSM_NORD	= FFFFFF7F		SYMSB_SEG	= 0000000C		WF	= 00008064	
PSCSM_NOSHR	= FFFFFFDF		SYMSB_TOKEN	= 0000000B		WG	= 0000A068	
PSCSM_NOVEC	= FFFFFDFF		SYMSK_BLKSIZ	= 0000000D		WH	= 00009070	
PSCSM_NOWRT	= FFFFFEFF		SYMSK_MAXLEN	= 0000001F		WL	= 00000064	
PSCSM_OVR	= 00000004		SYMSK_TWOCOL	= 00000010		WO	= 00000070	
PSCSM_PAGE	= 00006400		SYMSL_LINK	= 00000000		WORD	00000485 RG 04	
PSCSM_PIC	= 00000001							

WQ	= 00000068
WW	= 00000062
X	= 00000010
X1	= 00000033
X2	= 00080000
XFER	00000698 RG 04

! Psect synopsis !

PSECT name

	Allocation	PSECT No.	Attributes
. ABS	00000000 (0.) 00 (0.)	NOPIC USR	CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
. BLANK :	00000000 (0.) 01 (1.)	NOPIC USR	CON REL LCL NOSHR EXE RD WRT NOVEC BYTE
\$ABSS	00000013 (19.) 02 (2.)	NOPIC USR	CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
MACSRO_DATA	00000038 (56.) 03 (3.)	NOPIC USR	CON REL GBL NOSHR NOEXE RD NOWRT NOVEC LONG
MACSRO_CODE_P1	000006B7 (1719.) 04 (4.)	NOPIC USR	CON REL GBL NOSHR EXE RD NOWRT NOVEC LONG

! Performance indicators !

Phase

	Page faults	CPU Time	Elapsed Time
Initialization	29	00:00:00.02	00:00:02.02
Command processing	103	00:00:00.36	00:00:03.48
Pass 1	259	00:00:05.02	00:00:25.63
Symbol table sort	0	00:00:00.60	00:00:02.90
Pass 2	196	00:00:01.77	00:00:06.58
Symbol table output	43	00:00:00.22	00:00:01.00
Psect synopsis output	2	00:00:00.02	00:00:00.02
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	634	00:00:08.01	00:00:41.63

The working set limit was 1350 pages.

48829 bytes (96 pages) of virtual memory were used to buffer the intermediate code.

There were 40 pages of symbol table space allocated to hold 587 non-local and 67 local symbols.

1028 source lines were read in Pass 1, producing 29 object records in Pass 2.

21 pages of virtual memory were used to define 17 macros.

! Macro library statistics !

Macro library name

\$255\$DUA28:[MACRO.OBJ]MACRO.MLB;1
\$255\$DUA28:[SYSLIB]STARLET.MLB;2
TOTALS (all libraries)

Macros defined

15
3
18

625 GETS were required to define 18 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LI\$S:ACTSTA/OBJ=OBJ\$:ACTSTA MSRC\$:ACTSTA/UPDATE=(ENH\$:ACTSTA)+LIB\$:MACRO/LIB

0224 AH-BT13A-SE
VAX/VMS V4.0

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